

Coherent Phase Data Segment Layout in Data Storage Systems

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Field of the Invention

The present invention relates to organization of data in data storage systems, and more particularly, to organization of audio/video data stored in disk drives for uniform data retrieval rate.

Background of the Invention

Data storage systems such as disk drives are utilized for data storage and retrieval in a variety of applications. A typical disk drive includes a spindle motor for rotating a data disk, and an actuator for moving a head carrier that supports read/write heads radially across the disk to write data to or read data from concentric data tracks on the disk. Many disk drives include a plurality of disks separated by spacer rings and stacked on a hub attached to the spindle motor, a plurality of read/write heads, and a plurality of head carriers, each head carrier supporting at least one read/write head.

To access a data segment starting on a track, in a seek operation the head is moved radially across the tracks to the desired track where the data segment starts. Thereafter, the rotation of the disk rotates the data segment on the track under the head for writing data to or reading data therefrom. The data segment can continue onto one or more other tracks, wherein the head is sequentially moved to subsequent tracks for accessing the remainder of the data segment. The response time of the disk drive in

1 accessing a data segment includes the sum of three time periods: (1) the time period
2 for the actuator to move the head to the desired track (seek time), (2) the time period
3 for the start of the data segment to rotate under the head (rotation time or rotational
4 latency), and (3) the time period for recording or retrieving the entire data segment
5 (transfer time). The sum of the seek time and the rotational latency is also known as
6 access time.
7

8 A data rate, or throughput, in a disk drive with conventional data layout, is
9 determined as the ratio of the transfer time for the data in a selected data segment and
10 the response time for the data segment. For a randomly selected data segment, as in a
11 fragmented disk drive, the data rate for different data segments is random. FIG. 1
12 illustrates conventional layout of Logical Block Addresses (LBA) in a disk drive disk
13 space for maximizing forward sequential throughput. The layout is optimized for data
14 disk drives utilized for data storage in computer systems and maximizes forward
15 sequential throughput. Each data segment DS comprises two tracks TK shown in a
16 linear fashion. The advancing phase between sequential tracks TK as the disk rotates
17 is selected as a rational combination of the number of sectors or LBAs per track, X.
18 The number X is used as a modulus with a factor chosen so that the rotational time
19 associated with the phase advance (skew), is just greater than the time for the actuator
20 to move the head from track to track. The head movement can be either across the
21 surface of a disk or between heads to different disk surfaces.
22

23 Such conventional layouts usually result in good performance for disk drives
24 utilized in computer applications as measured by various well known benchmark
25 programs. However, a disadvantage of such layouts is the poor performance of the
26 disk drive when used for storing Audio Visual (AV) content. For example, the disk drive
27 response time when moving sequentially backward through the content (as for reverse
28 play or reverse search) is significantly higher than when moving forward through the

1 content. This is because as shown in FIG. 1, the starting and ending phase of any data
2 segment DS, and accordingly its rotational phase relative to other data segments, is
3 different according to its track. For a randomly selected data segment DS, the
4 rotational phase or time to the next randomly selected data segment DS is a random
5 variable. The phase difference (rotational phase) between the data segments is
6 incoherent. As such, when moving the head from one data segment to another data
7 segment, especially when serving more than one AV stream, a wide range of rotational
8 phases and rotate times are encountered and the averages and maximum response
9 times are adversely affected.

10
11 Further, in conventional disk drives, the seek time and the rotational latency are
12 treated separately, though access time is more important for any application than the
13 individual time periods. The seek time and rotational latency are combined by default
14 through a computer file system and a default access time results for a particular system
15 and application. Therefore, the seek time and the rotational latency are not utilized in
16 combination for efficient operation of the disk drive in reducing access time. As such in
17 many systems, a seek control system moves the head to a target track as fast as
18 possible and then on average waits one half a revolution for the data to rotate under the
19 head ("hurry up and wait"). Fast movements of the head result in unwanted acoustic
20 noise, and lead to high power consumption.

21
22 In some disk drives an attempt has been made to overcome the above problems
23 by engineering the file system. However, the disk drive designer has incomplete
24 knowledge of the physical layout of the data disk, and such knowledge can quickly
25 become obsolete. As a result, the disk drives are overdesigned as described above,
26 with resulting cost penalties. Still other attempts have been made to overcome the
27 rotational latency and seek time disconnect. One such attempt is seek reordering
28 wherein the order of requests to the disk are reordered to minimize the access time

1 over several such requests. Though this method can be effective in disk drives utilized
2 in computer systems for computer applications, it is quite ineffective for support of
3 multiple AV streams. This is because the isochronous nature of AV streams makes
4 order of requests for multiple AV streams important. As such, changing the order of
5 requests results in failure to record or retrieve the correct AV data at the correct time.

6
7 There is, therefore, a need for a system and method for improving the systematic
8 reliability of response time and consequently the sustained throughput of disk drives.
9 There is also a need for such a system and method to allow for improvement in
10 managing seek acoustic noise.

11 12 Summary of the Invention

13 The present invention satisfies these needs. In one embodiment the present
14 invention provides a method and data layout/pattern for organizing and allotting disk
15 drive capacity in a data storage system including data storage media having at least
16 one recording surface. A method for storing at least one set of data segments to said
17 recording surface in concentric data tracks includes the steps recording each data
18 segment onto said recording surface such that each stored data segment has a start,
19 an end and a rotational phase from that data segment to each of the respective ones of
20 all other data segments, wherein the data segments are recorded with coherent relative
21 rotational phases. The coherent phase layout provides substantially constant data
22 transfer rate to/from the storage media because the relative rotational phase or
23 rotational time from one data segment to another data segment is deterministic.

24
25 The relative rotational phases are predetermined, and each relative rotational
26 phase can have one of a limited number of predetermined values. In one version, the
27 relative rotational phases from each data segment to respective ones of a first subset of
28 the data segments in the set have one of said predetermined values, and the relative

1 rotational phase from that data segment to respective ones of a second subset of the
2 data segments in the set have another of said predetermined values. Each data
3 segment can include one or more tracks, and data tracks in that data segment can be
4 offset by a predetermined skew angle. Alternatively, each track can include one more
5 data segments.

6
7 In one version, one or more incoming data streams are received and partitioned
8 into data segments for storage in coherent phase. The data segments are recorded so
9 as to obtain a nearly constant data storage transfer rate when reading the data from the
10 data storage media. The data segments read from the storage media are combined to
11 reformulate one or more data streams from the data segments. In one embodiment of
12 the present invention, the data storage system is a component of a computer system.
13 In another embodiment, the data storage system can be a component of an audio video
14 storage server. In that case, the data segments comprise audio visual data and the
15 method of the present invention is used to store and retrieve isochronous Audio-Video
16 (AV) content for consumer electronics applications.

17
18 The present invention further provides a seek profile for disk drive including a
19 transducer radially moveable relative to the tracks on a disk by an actuator controlled by
20 a servo circuit during a seek operation from a starting segment to a destination
21 segment. Data is stored on the disk in segments with the coherent phase layout.
22 Performing a seek operation from a starting segment to a destination segment includes
23 obtaining a seek profile for controlled application of current to the actuator based on the
24 seek profile, wherein the seek profile includes constraints for the seek operation as a
25 function of: (1) a seek distance representing the radial distance between the starting
26 and destination segments, and (2) a seek time based at least on the relative rotational
27 phase between the starting and destination segments. Current is then applied to the
28 actuator as a function of said constraints to perform the seek operation. In one version,

1 each seek operation is completed at the expiration of the respective seek time, and for
2 at least one set of seek distances, the respective seek times are predetermined.
3 Further, the seek time between two data segments can be based on the relative
4 rotational time between the data segments, wherein regardless of the seek distance
5 between the two data segments, the seek operation need only be completed at the end
6 of the respective seek time, not before. As such, in one version, wherein a set of data
7 segments have the same inter-segment rotational time (or relative rotational phase), the
8 seek time for each seek operation among two data segments in the set is the same,
9 and preferably equal to said inter- segment rotational time. The actuator need only
10 move the transducer as fast as needed to cover the seek distance by the end of the
11 seek time, and not before. Therefore, for short seek distances in relation to long seek
12 distances, the actuator moves the transducer at a lower velocity than for long seek
13 distances, to cover the seek distance at the end of the seek time. This reduces seek
14 acoustic noise.

15
16 The present invention improves the systematic reliability of response time and
17 consequently the sustained data rate or throughput, taking into account the electro-
18 mechanical nature of a disk drive. At the same time the present invention preserves
19 the random access nature of a disk drive, and the new benefits derived therefrom for
20 the storage of AV content. Additionally, the present invention allows for significant
21 improvement in managing seek acoustic noise.

22 23 **Brief Description of the Drawings**

24 These and other features, aspects and advantages of the present invention will
25 become better understood with regard to the following description, appended claims
26 and accompanying drawings where:

27 FIG. 1 shows a conventional disk track layout for a disk drive;

28 FIG. 2 shows an example computer system including a disk drive with a data

1 pattern layout according to one aspect of the present invention;

2 FIG. 3 depicts a top plan view of a disk drive head and disk assembly (HDA) and
3 a block diagram of disk drive electronics of the disk drive of FIG. 2 that implement and
4 utilize principles of the present invention;

5 FIG. 4 shows a block diagram of the architecture of an embodiment of the drive
6 electronics of the disk drive of FIG. 2;

7 FIG. 5 shows an embodiment of data pattern layout in a linear fashion for the
8 disk drive according to the present invention;

9 FIG. 6A shows an embodiment of data pattern layout of FIG. 5 in concentric
10 tracks according to the present invention;

11 FIG. 6B shows an embodiment of data pattern layout in different recording
12 zones;

13 FIG. 7A shows a functional diagram of an aspect of the servo controller of FIG. 4
14 operating from seek and transducer motion information in seeking operations;

15 FIG. 7B shows an example flow diagram of an embodiment of steps for
16 performing seek operations according to the present invention;

17 FIG. 8A shows example performance values for a disk drive according to the
18 present invention;

19 FIG. 8B shows a plot of performance indicia for a disk drive according to the
20 present invention;

21 FIG. 9 shows an example block diagram of an AV storage server and AV
22 system according to another aspect of the present invention;

23 FIG. 10 shows a flow diagram of an embodiment of a process for storing data in
24 a disk drive according to the present invention;

25 FIG. 11 shows a flow diagram of an embodiment of a process for retrieving data
26 in a disk drive according to the present invention;

27 FIG. 12A shows another embodiment of a data pattern layout in a linear fashion,
28 according to the present invention, wherein each data segment is one-half track in size;

1 and

2 FIG. 12B shows another embodiment of a data pattern layout in a linear fashion,
3 according to the present invention, wherein each data segment is one-third track in
4 size.

5
6 To facilitate understanding, identical reference numerals have been used, where
7 possible, to designate identical elements that are common throughout the figures.
8

9 **Detailed Description of the Invention**

10 FIG. 2 shows a block diagram of an example computer system 10 in which a
11 method embodying aspects of the present invention can be implemented. The
12 computer system 10 includes a central processing unit ("CPU") 14, a main memory 16,
13 and I/O bus adapter 18, all interconnected via a system bus 20. Coupled to the I/O bus
14 adapter 18 is an I/O bus 22 that can comprise e.g. a small computer system
15 interconnect (e.g., SCSI, ATA (IDE), 1394, etc.) bus, and which supports various
16 peripheral devices 24 including a storage device/unit such as a disk drive 25. The disk
17 drive 25 includes drive electronics 26 and a head disk assembly 28 ("HDA"). The
18 computer system 10 can further comprise a network interface device 30 connected to
19 the bus 20 for data communication between the computer system 10 and other
20 computer systems 32 via a network link 34 in a networked data processing system 36.
21

22 FIG. 3 depicts a top plan view of the HDA 28 and the drive electronics 26 of the
23 disk drive 25, incorporating principles and aspects of the present invention therein. The
24 HDA 28 includes a rotatable magnetic storage disk 38, a DC brushless in-hub spindle
25 motor (not specifically shown), a hub 40 containing and/or enclosing the spindle motor
26 and spindle bearings, a rotary voice coil actuator assembly 42, a read preamplifier/head
27 select/write driver circuit 44 connected to the rotary actuator by a flex circuit 46 enabling
28 the HDA 28 to be connected to the disk drive electronics 26 mounted to the HDA 28

1 externally of the interior thereof, and a base housing 50 to which the various
2 components of the disk drive are mounted and aligned.

3
4 Typically, the storage disk 38 is coated with a magnetic material that stores data
5 in the form of longitudinal bipolar magnetic patterns written by digital saturation
6 recording techniques within each concentric data track. For simplicity, the following
7 discussion mentions only a single storage disk 35 in the disk drive 25. However, as
8 those skilled in the art will recognize from the following discussion, the present invention
9 is capable of use in disk drives having multiple disks mounted upon the spindle hub 40,
10 the number of disks 38 and associated data transducer heads affecting the vertical
11 height of the disk drive.

12
13 The actuator assembly 42 conventionally comprises a transducer head gimbal
14 assembly 52 for each disk data surface, a carriage assembly 54, and a rotary voice coil
15 actuator motor (VCM) 56. In the rotary-type actuator assembly 42, the transducer head
16 gimbal assembly 52 is attached to an outer end 58 of the carriage 54, while the actuator
17 motor voice coil 56 is attached at a hub end 60 of the carriage 54. Centrally located
18 along the carriage is a pivot 62 about which the actuator assembly rotates on a dual
19 bearing assembly secured to the base housing 50. The pivot 62 is located adjacent the
20 storage disk 38 such that the carriage 54 extends the upper and lower transducer head
21 gimbal assemblies 52 over the surfaces of the storage disk 38. Consequently, selective
22 activation of the actuator voice coil motor (VCM) 56, rotates the actuator assembly 42
23 about the pivot 62 and accurately positions each transducer assembly 52 over the
24 surface of the storage disk 38. As such, data can be written to, and can be read from,
25 each data storage surface of the storage disk 38 by transducers within the transducer
26 assembly 52.

27
28 Typically, the transducer assembly 52 includes a dual head transducer assembly

1 64 including e.g. a thin film inductive write head and a shielded magnetoresistive thin
2 film read element (not shown). The dual head transducer assembly 64 is formed at an
3 outer end of one rail of e.g. a two rail aerodynamic slider 70, as shown for example in
4 FIG. 3. In accordance with conventional practice, the magnetoresistive (MR) read
5 element is formed on the slider first, in order to take advantage of the smoothness of
6 the finished slider end surface. After the MR read element is formed, the thin film
7 inductive element is formed over the MR read element.

8
9 A flexure assembly 72 includes a gimbal secured to the slider and to a load
10 beam. The load beam provides a preloading gram force to the slider to bias it toward
11 the facing disks storage surface. The flexure assembly 72 connects at one end to the
12 carriage; another end supports a slider 70 and the read/write elements over the disk
13 surface. When the storage disk 38 is not rotating, the slider 70 and transducer
14 assembly 64 rest upon a radially inward landing zone of the disk surface. On the other
15 hand, when the storage disk 38 is rotating, the slider 70 overcomes the load beam
16 spring bias and "flies" several microinches above the disk surface on an "air bearing" in
17 accordance with what is known in the art as Winchester technology.

18
19 During flight, the actuator assembly 42 positions the transducers in the
20 transducer assembly 64 over the multiplicity of concentric data tracks 74 and data
21 segments DS defined on one, or the oppositely facing, storage surface of the storage
22 disk 38 so as to read servo and user data and to write user data. However, when the
23 disk drive 25 is deactivated, the sliders 70 are moved in unison by the carriage
24 assembly 54 to the inner landing zone and "parked" such that they will not damage the
25 surface of the disk 38 by coming into contact with it.

26
27 The disk drive preamplifier/write driver circuit 44 is connected, via a flex circuit
28 78, to the actuator assembly 42 so that electrical signals may reach the head

1 transducer assemblies 64 via minute wires carried along the side of the carriage 54 and
2 load beam. The signals leaving and entering the HDA 28 via the flex cable 46 are
3 utilized by a drive microcontroller 80 and other electronics including a motors control
4 driver ASIC 82 which supplies driving signals to operate the spindle motor and the
5 rotary actuator, a PRML read/write channel 44 which receives and decodes coded data
6 from the disk and which encodes and delivers coded data to the write driver portion of
7 the IC 44.

8
9 A disk drive electronics ASIC 84 implements a SERDES/ENDEC function, and
10 ECC function, a data sequencer, a memory controller, a bus level interface, and a
11 microprocessor interface for interfacing the microprocessor 80 with other circuits
12 including a DRAM buffer 86 which can include microprocessor program instructions,
13 seek profiles, data blocks being transferred between a host (not shown) and the data
14 storage disk 38, etc.. The microprocessor 80 implements a servo loop for controlling
15 positioning (following, seeking, etc.) of the rotary actuator 42. In one example, the
16 memory 86 includes program instructions for execution by the microprocessor 80 to
17 implement the servo loop.

18
19 During track settling and following operations, the servo loop receives actual
20 position samples from position information within embedded servo wedges via the read
21 element, and separately estimates head position, velocity and actuator bias force in
22 order to generate and put out a control command value via the motors control ASIC 82
23 to control head position. During each seek operation from a starting track/segment to a
24 destination track/segment, the servo loop utilizes a seek profile in conjunction with head
25 motion information (e.g., position, velocity, etc.) to determine the control command
26 value for moving the transducers from the starting track/segment to the destination
27 track/segment. An internal data, address, control bus structure 88 interconnects the
28 microprocessor 80, motors control ASIC 82, PRML read/write channel ASIC 44, disk

1 drive electronics ASIC 84 and DRAM buffer chip 86. A connection to the host
2 computing equipment is provided by a drive interface bus 90.

3
4 Referring to FIG. 4 in conjunction with FIG. 2, in another embodiment, the drive
5 electronics 26 of FIG. 4 is shown to include a data controller 92 interconnected to a
6 servo controller 94 via bus 96, and a read/write channel 98 interconnected to the data
7 controller 92 via a data buffer bus 100. Actual positioning information from data disks
8 are induced into the transducers, converted from analog signals to digital data in the
9 read/write channel 98, and transferred to the servo controller 94, wherein in a servo
10 loop the servo controller 94 utilizes the head positioning information for performing
11 seeking and tracking/following operations of transducers over the disk tracks 74. In one
12 example, the servo controller can include the microprocessor 80 of FIG. 3, for executing
13 program instruction to implement the servo loop.

14
15 A typical data transfer initiated by the CPU 14 to the disk drive 25 may involve for
16 example a direct memory access ("DMA") transfer of digital data from the memory 16
17 onto the system bus 20 (FIGS. 2 and 4). Data from the system bus 20 are transferred
18 by the I/O adapter 18 onto the I/O bus 22. The data are read from the I/O bus 22 by
19 the data controller 92, which formats the data into data blocks with the appropriate
20 header information and transfers the data to the read/write channel 98. For example,
21 the data controller 92 collects data into data blocks or segments and appends Error
22 Detection and Correction bits to the blocks. These blocks are further collected into a
23 service unit which is then passed to the read/write channel 98. Concurrently, a
24 command is issued to the servo controller 94 to cause the actuator 42 to move the
25 actuator assembly 64 to the appropriate cylinder. One or more of said function can be
26 implemented external to the disk drive as a software driver running in a host processor
27 (e.g., CPU 14), such as for simple ATA disk drives. The read/write channel 98 operates
28 to convert data between the digital form used by the data controller 92 and the analog

1 form suitable for writing to data disks by transducers in the HDA 28.

2
3 For a typical request for transfer of data from the HDA 28 to the CPU 14, the
4 data controller 92 provides a disk track/segment location to the servo controller 94
5 where the requested data is stored. In a seek operation, the servo controller 94
6 provides control signals to the HDA 28 for commanding the actuator 42 to position the
7 transducer assembly 64 and transducers therein over said disk track/segment for
8 reading the requested data therefrom. The read/write channel 98 converts the analog
9 data signals from the transducers into digital data and transfers the data to the data
10 controller 92. The data controller 92 places the digital data on the I/O bus 22, wherein
11 the I/O adapter 18 reads the data from the I/O bus 22 and transfers the data to the
12 memory 16 via the system bus 20 for access by the CPU 14.

13
14 Referring FIG. 5, in one embodiment of the present invention, each surface of a
15 disk 38 carries a multiplicity of spaced apart concentric tracks 74. Each track 74 is
16 divided into an equal number of circumferential divisions. These divisions are generally
17 arced along the disk radius in accordance with an arc defined by the head and the
18 rotary actuator. Each division begins with a servo sector or "wedge" and is followed by
19 a user data sector. The head position servo information is included in each servo
20 wedge and the user data is recorded in each data sector. Because the servo
21 information is included on the data surface, the servo sectors are said to be
22 "embedded" in that they lie interspersed among the data sectors. Each servo wedge
23 contains information used for accurate positioning of the transducers over each
24 selected data track, so that user data may be written to, or read from, an adjacent data
25 sector.

26
27 As shown in FIG. 5, in one embodiment of the present invention data segments
28 DS including user data are stored on the disk 38, wherein each data segment DS

1 comprises one or more tracks 74. FIG. 5 shows an example track layout on the disk 38
2 in a linear fashion, wherein the data segments DS have coherent phase. As such, each
3 data segment DS has a start "S", an end "E" and a relative rotational phase "R" relative
4 to other data segments DS. Each data segment DS has a predetermined rotational
5 phase to another data segment DS. For each data segment DS, the rotational phase R
6 from that data segment DS (e.g., end of the data segment) to each of the other data
7 segments DS is predetermined, and can be selected from a limited range of values. As
8 such, if a set of data segments includes ten data segments, each data segment DS has
9 a distinct, but no necessarily different in value, predetermined rotational phase R
10 relative to each of the other nine data segments in the same. In that example, two or
11 more of the nine distinct relative rotational phases from said one data segment to the
12 other nine data segments in the set can have the same predetermined value (e.g., FIG.
13 5). Further, different subsets of the nine rotational phases can have different
14 predetermined values (e.g., FIGS. 12A-B).

15
16 A phase difference between the start of a data segment DS and the start of
17 another data segment DS is defined as a relative start phase (e.g., 0 in FIG. 5), and a
18 phase difference between the end of a data segment and the end of another data
19 segment is defined as a relative end phase (e.g., 0 in FIG. 5). In one layout version, the
20 relative rotational phase of any data segment to any other data segment DS is
21 independent of the start or end tracks of the data segments DS. Similarly, the relative
22 start phase for any data segment DS is predetermined, and the relative end phase for
23 any data segment DS is also predetermined, independent of the start or end tracks of
24 the data segment DS.

25
26 In the example layout shown in FIG. 5, the start of each data segment DS is
27 defined as 0, and the end of each data segment DS is defined as $360 + (N-1)\alpha$, where
28 N is the number of tracks 74 in a data segment DS and α is the skew angle between

1 tracks within a Data segment DS. The rotational phase distance R from the end of a
 2 data segment DS to the start of any subsequent data segment DS, adjacent or
 3 otherwise, is predetermined and the same (e.g., $R=360-(N-1)\alpha$). As such, the rotation
 4 time period from the end of a data segment DS to start of any subsequent data
 5 segment DS (inter-segment rotational time) is predetermined and the same. The skew
 6 angle α is selected to be greater than or equal to track-to-track seek time (e.g., between
 7 about 0.2×360 to about 0.3×360 degrees). The skew angle α can be adjusted to refine
 8 average and worst case response times. In FIG. 5, for the data segment DS, the starts
 9 are the same and constant (i.e., 0 degrees), the ends are the same and constant (i.e.,
 10 $360+\alpha$ degrees), the relative start and end phases are the same and constant (i.e., 0
 11 degrees), and the relative rotational phased R are the same and constant (i.e., $360-$
 12 α degrees).
 13

14 The rotational phase R can be adjusted to allow skew angle between tracks for
 15 a given number of tracks per data segment DS to improve the average response time
 16 for a randomly selected data segment DS. Preferably, the skew between tracks within
 17 a data segment (e.g., the rotational angle between start of tracks, or the resulting track-
 18 to-track seek time -- track head switch time) is selected to be just greater than the
 19 maximum time for the actuator to move the head the distance of one track.
 20

21 FIG. 6A shows another diagram of example track layout of FIG. 5, and FIG. 6B
 22 shows an embodiment of data pattern layouts in different recording zones 91, described
 23 further below. Referring to FIGS. 5 and 6A, data segment DS0 comprising tracks TK0,
 24 TK1; data segment DS1 comprising tracks TK2, TK3; data segments DS2 comprising
 25 tracks TK4, TK5; and data segment DS3 comprising tracks TK6, TK7 are shown.
 26 Tracks TK0, TK2, TK4 and TK6 have the same start and end relative to each other.
 27 And, tracks TK1, TK3, TK5, and TK7 have the same start and end relative to each
 28 other. The skew angle between track pairs TK0, TK1 is shown as α . Similarly, the

1 skew angle between track pairs TK2, TK3; TK4, TK5; and TK6, TK7, is shown as α . In
2 this example, for each data segment DS the rotational phase R, or rotational time, to a
3 subsequent data segment is always the same (i.e., $360-\alpha$).
4

5 To provide a substantially constant data transfer rate, a seek profile for the servo
6 system can be designed according to seek time constraints such that the seek time
7 between any two data segments in a set of data segments (e.g., data segments in a
8 recording zone 91) is selected to be a predetermined value. For example, the seek
9 time can comprise the maximum seek time that is necessary to seek from one data
10 segment DS to another data segment DS. As such, if for example 10 msec is required
11 to seek from DS0 to DS1, then seeking from DS1 to DS4 also requires 10 msec. For
12 short seeks (e.g., DS0 to DS1) compared to long seeks (e.g., DS0 to DS11), the
13 servo system can take the entire 10 msec for the seek operation and move the head
14 slower by inputting less power into the actuator (thereby reducing acoustic noise).
15 Other predetermined seek times can also be selected to achieve substantially constant
16 data transfer rate to and from the disk.
17

18 When the seek performance requirement is so selected, the seek noise can
19 accordingly be reduced significantly. This is a highly valuable attribute for hard disk
20 drives in A/V systems and A/V applications. For example, seek acoustic noise can be
21 dramatically reduced by implementing a specification for the seek servo system (seek
22 profile) according to Table 1 below. Additionally, the cost of seek servo amplifier and
23 actuator motor can be reduced.

Seek Distance	Seek Time
One track and head switches (for intra-DS moves)	2 msec
Two tracks to two thousand tracks	8 msec
Two thousand one tracks to full stroke	19 msec

Table 1. Example seek servo spec. for coherent data segment layout

In the context of Audio and/or Video data and streams, when the data is organized according to the present invention, the seek performance requirement is systematically relaxed. The average and worst case response times are improved. The reduction of the average and worst case response time is also a benefit of the present invention in cases where e.g. a large number of simultaneous streams are being serviced and/or the content has become fragmented physically.

In one embodiment, as a result of a data segment layout according to the present invention, wherein the rotational phase R (and inter-segment rotational time) from a data segment DS to the start of any subsequent data segment DS is predetermined (e.g., the same, or selected from a limited number of predetermined values, etc.), seeks requiring less time than the inter-segment rotational time need not be completed any faster. As such, the seek time is selected based on the inter-segment rotational time, allowing a larger number of possible seek distances can be completed within an inter-segment rotational time.

An example servo loop implementing predetermined seek times according to the present invention is now described. Referring back to FIG. 4, for each seek operation to transfer data, the data controller 92 provides a destination track/segment DS (in

1 relation to the current position of the head at a starting track/segment DS), to the servo
2 controller 94 where data is to be transferred to/from a segment DS. The servo
3 controller 94 provides control signals to the HDA 28 for commanding the actuator 42 to
4 position the transducer over said destination track/segment DS for transferring data.
5 For example, for a read operation, the read/write channel 98 converts the analog data
6 signals from the transducers into digital data and transfers the data to the data
7 controller 92. The data controller 92 places the digital data on the I/O bus 22, wherein
8 the I/O adapter 18 (FIG. 2) reads the data from the I/O bus 22 and transfers the data to
9 the memory 16 via the system bus 20 for access by the CPU 14.

10
11 In one version, the servo controller 94 implements a servo loop, and uses seek
12 profiles for performing seek operations. For each seek operation from a starting
13 track/segment DS to a destination track/segment DS, the servo controller 94 receives
14 actual position samples from position information within embedded servo wedges via
15 the read element in the head, then estimates head position and velocity, and uses a
16 seek profile to generate and put out a control command value $I_{command}$ to the
17 actuator VCM to move the transducers from the starting track/segment DS to the
18 destination track/segment DS.

19
20 Destination segment DS position data on bus 96 provides coarse positioning
21 information to the servo controller 94 for specifying a seek distance representing the
22 radial distance that the actuator 42 must move the transducer from the starting segment
23 DS (e.g., end of starting segment) to reach the destination segment DS (e.g., start of
24 destination segment). The seek time comprises a predetermined time period for the
25 transducer to cross over the tracks between the starting segment DS and the
26 destination segment DS in the seek distance. The servo controller 94 uses seek
27 information, including e.g. seek distance, on bus 96 and servo head position
28 information on line 99 to generate a current value $I_{COMMAND}$ according to the seek

1 profile, to control supply of input actuator current I_a to the VCM, resulting in controlled
2 movement of the actuator assembly 64 such that the seek operation is completed by
3 the end of the seek time, and preferably not sooner. As such, according to the seek
4 profile, the servo controller 94 generates the current command value $I_{command}$ such
5 that each seek operation is completed at the expiration of the respective predetermined
6 seek time.

7
8 For the example layout pattern of FIG. 5, the seek time is selected to be the
9 relative rotational time (inter-segment rotational time based on relative rotational phase
10 R) between segments DS. Because in the pattern of FIG. 5 the relative rotational
11 phase R for all the segments DS is the same, the seek time from each starting segment
12 DS to any destination segment DS is the same, regardless of the seek distance in
13 tracks between the starting and destination segments. Seeks requiring less time than
14 the inter-segment rotational time need not be completed any faster. Accordingly, if for
15 example 20 msec is required to seek from DS0 to DS2, then seeking from DS1 to DS4
16 also requires 20 msec. For short seeks (e.g., DS0 to DS1) compared to long seeks
17 (e.g., DS0 to DS11), according to an example seek profile the servo controller 94 uses
18 the entire 20 msec for the seek operation and moves the head slower by inputting less
19 power into the actuator (thereby reducing acoustic noise). As such, according to the
20 example seek profile, for short seeks the servo controller 94 commands less current
21 input to the VCM to move the actuator, and for longer seeks, the servo controller
22 commands more current input to the VCM to move the actuator faster. In either case,
23 the seek distance is traversed by the transducer such the each seek operation is
24 completed by the end of the same seek time, and not sooner.

25
26 The seek profile can include constraints such as feed current values, target
27 velocity per distance, expected distance to go, etc. for seek operations. In one version
28 of the servo controller 94, for each seek operation, using the seek distance and the

1 predetermined seek time for the seek operation, the servo controller 94 obtains current
2 level constraints from the seek profile, to generate the current command for the seek
3 operation. Then, during the seek operation, the servo controller 94 receives actual
4 head position information, and compares the actual head position information with the
5 expected head position information according to the seek profile, and adjusts the
6 current command value to the actuator 42 as necessary to move the transducers
7 according to the seek profile, such that the seek is completed by the end of the seek
8 time, not necessarily sooner.

9
10 For generating the current command, in one embodiment a feed current value
11 FC is obtained or calculated by a microcontroller (e.g., microprocessor 80 in FIG. 3) in
12 the servo controller 94 to provide a base current value, depending upon the seek
13 distance and seek time. The feed current value FC is an a priori prediction of current
14 expected to be required to carry out the seek operation to achieve the seek
15 performance described herein (e.g., Table 1) based on a data layout according to the
16 present invention. It is based on information which quantify the operating
17 characteristics, some of which are developed during initialization calibration routines,
18 and can be stored in memory. The feed current value FC allows more accurate
19 adherence to a desired seek trajectory. The feed current value FC corresponds to the
20 actuator current needed to keep the actuator on the idealized trajectory.

21
22 In this example, fundamentally, a nominal current waveform is preestablished for
23 each seek distance to be traversed by the expiration of the respective predetermined
24 seek time (preferably not sooner), wherein the seek time is based e.g. on the relative
25 rotational phase R (or inter-segment rotation time) between the starting segment DS
26 (e.g., end of starting segment) and the destination segment DS (e.g., start of
27 destination segment).

1 In versions where all segments DS in a set of segments have the same relative
2 rotational phase R (e.g., FIG. 5), the seek time can be the same for all seek distances
3 between all segments DS in that zone. In versions where the relative rotational phase
4 between a set of segments DS (e.g., segments in a recording zone 91) has one of
5 several predetermined values (e.g., FIGS. 12A-B), the seek time can have one or more
6 predetermined time values based at least on the values of the relative rotational phases
7 (e.g., seek time can be a linear function of inter-segment rotation time). The seek time
8 can also be selected to be a maximum, minimum, or combination of said
9 predetermined time values for all seek distances.

10
11 FIG. 7A shows an example functional block diagram of an embodiment of the
12 servo controller 94 including the microcontroller 80 of FIG.3, as configured by process
13 steps according to the present invention to perform seek operations according to seek
14 profiles 93, operating from seek and transducer motion information including e.g. actual
15 head position, transducer velocity, transducer distance to destination segment DS, etc.
16 in seeking operations. In this example the seek profile 93 includes e.g., a priori feed
17 current values FC 93A, expected velocity/position reference values 93B, etc. The a
18 priori feed current values FC for the basic current corresponding to the seek distance
19 and seek time can also be calculated by the microcontroller 80 based on seek
20 information which can reside in memory.

21
22 In one example, the feed current values FC (e.g., current profiles) are stored in
23 memory as a FC look-up table of entries 93A, wherein each entry includes a current
24 value FC, indexed by the seek distance and the seek time. Each feed current profile
25 comprises an a priori prediction of current expected to be required to carry out a seek
26 operation (e.g., transducer traversing a seek distance) by the end of the respective
27 seek time, not sooner. In one version of the servo controller 94, for each seek
28 operation, using the seek distance and the predetermined seek time for the seek

1 operation, the servo controller 94 obtains current level constraints from the seek profile,
2 to generate the current command for the seek operation. Then, during the seek
3 operation, the servo controller receives actual head position information, and in a
4 regulation process 95 compares the actual head position information with expected
5 head position information 93B (EV) according to the seek profile, and adjusts the
6 current command value in a regulate FC process 95 as necessary to move the head
7 according to the seek profile, such that the seek is completed by the end of the seek
8 time, not necessarily sooner.

9
10 In another version of the servo controller 94, for each seek operation, using the
11 seek distance and the predetermined seek time for the seek operation, the servo
12 controller 94 obtains current level from the seek profile 93, to generate the current
13 command for the seek operation. Then, during the seek operation, the servo controller
14 receives actual head motion information, and compares the actual head motion
15 information with the according to the seek profile, and adjusts the current command
16 value as necessary to move the head according to the seek profile, such that the seek
17 is completed by the end of the seek time, not necessarily sooner. For example, the
18 expected values can include transducer motion constraints such as target velocity,
19 expected velocity, expected target velocity per distance, expected distance to
20 destination segment DS, expected transducer position, etc. in a table 93B.

21
22 In one implementation, an actuator feed current profile FC in table 93A is
23 predetermined and provides the amount of actuator current per distance from the
24 destination track. The predetermined look-up table 93B specifies the expected
25 transducer velocity per distance from the destination track, EV, for each predetermined
26 seek time. As such, in the regulation process 95, for each detected transducer location,
27 a detected radial velocity is subtracted from the corresponding expected radial velocity.
28 The difference is then used to adjust the profile current value for that detected

1 transducer location to provide the feed current values FC such that the seek distance is
2 traversed by the end of the seek time.

3
4 FIG. 7B shows an example flow diagram of an embodiment of steps performed
5 by the servo controller 94 for performing seek operations. The microcontroller 80 in the
6 servo controller 94 executes program instructions including the regulation process 95.
7 For a seek operation, the microcontroller 80 obtains the distance between the
8 transducer location and the destination track/segment (seek distance), and obtains the
9 seek time for the seek operation (step 101) . In one example, the predetermined seek
10 time between each pair of segments DS (e.g., identified by unique number or location
11 on reference disk) in a recording zone is stored in memory. In another example, the
12 predetermined seek time between each pair of segments DS is obtained based on the
13 inter-segment rotational time between the two segments DS using the angular location
14 of the end of the starting segment DS and the angular location of the start of the
15 destination segment DS on the recording surface (i.e., relative rotational phase R). The
16 microcontroller 80 then obtains a corresponding FC value from the FC look-up table
17 93A using the seek distance and the respective predetermined seek time (step 103).

18
19 The microcontroller 80 used actual head position information, and subtracts
20 current head position track from the destination segment track to determine a total
21 number of tracks remaining to be crossed (step 105). The microcontroller 80
22 calculates the actual radial velocity of the transducer as the number of tracks crossed
23 over by the transducer in between two sampling intervals (step 109), where a sampling
24 interval is defined by a servo wedge passing under the transducer head. As such,
25 during each sampling interval, the transducer location is detected, and the
26 microcontroller 80 calculates the radial velocity of the transducer by determining a
27 difference in the number of tracks between: (1) a first transducer location detected
28 during a present sampling interval, and (2) a second transducer location detected

1 during a preceding sampling interval. The microcontroller 80 determines the track
2 difference between the two transducer locations to obtain the number of tracks crossed
3 over by the transducer between the two sampling intervals, providing a measure of the
4 actual radial velocity of the transducer. The radial velocity can be expressed as tracks
5 crossed over per sampling interval (e.g., tracks/sample).
6

7 The actual velocity and distance to go values are applied to address and
8 compare to an expected trajectory value (e.g., velocity per distance remaining) stored in
9 the trajectory profile look-up table (e.g. table 93B) in memory (e.g. DRAM 86) (step
10 111). A ratio value, V_r , of actual velocity value and the reference velocity value from
11 said look-up table is calculated (step 113) in order to normalize the difference between
12 the actual velocity and the reference velocity irrespective of magnitude thereof. The
13 feed forward waveform value FC corresponds to the actuator current needed to keep
14 the actuator assembly 64 on the idealized trajectory.
15

16 The normalized, signed velocity signal V_r is added to the feed forward value FC
17 to correct for any deviations from the reference trajectory due to friction, torque
18 constant variation, etc. (step 115). As such, the normalized error signal V_r is added to
19 the priori feed forward value FC which yields a corrected actuator current command
20 value I_{Command} . The process is repeated till the destination track/segment DS is reached
21 (step 107). Other methods of implementing servo loops to perform the seek operations
22 based on predetermined seek times according to the present invention are possible,
23 and contemplated by the present invention. For instance, an example feed current
24 calculation based on expected velocity is described in the commonly assigned United
25 States Patent No. 5,005,089, titled "High performance, high capacity micro-Winchester
26 disk drive", incorporated herein by reference, and can be modified to implement seek
27 operations according to the predetermined seek times described herein.
28

1 According to the present invention, seek profile specification is given by required
2 seek time performance across the tracks on the disk, wherein in one example the seek
3 time is stepped (rather than conventional linear function) with a seek time for 1 track
4 seeks, another seek time for seek distances of 2 to x tracks, another seek time for
5 seek distances greater than x by less than y, etc. (e.g., Table 1). As such, excitation of
6 the electromechanical system of the servo control can be relaxed, making the disk drive
7 quieter. Using a coherent phase data layout pattern according to the present invention.
8 A seek profile is selected to move the transducer from a starting segment to arrive at a
9 track where the destination segment starts, just before the start of the destination
10 segment rotates under the transducer, not sooner.

11
12 Preferably, the seek time is substantially equal to the inter-segment rotational
13 time between the end of the starting segment to the start of the destination segment.
14 For example using Table 1 above, to seek from a starting segment to a destination
15 segment, the servo controller 94 determines how far the destination data segment is in
16 tracks (seek distance), and then if the seek distance is within e.g., 2 - 2000 tracks, the
17 transducer can take 8 msec to get to the destination segment (seek time = 8 msec). If
18 the seek distance is more than 2000 tracks, then the seek time is 19 msec. As such,
19 according to one version of the present invention, transducer position information from
20 the read/write channel, the physical layout of the data with coherent phase, and the
21 servo loop using seek times based on inter-segment rotational time, are used together
22 to obtain nearly constant data transfer rate, and operate the disk drive quietly and with
23 reduced power.

24
25 A data segment DS can be selected to be the amount of physically contiguous
26 data (e.g., bytes, sectors, tracks, etc.) recorded or retrieved to or from the disk 38 each
27 time the actuator moves the heads 52 (e.g., in a skip-sequential manner). A data
28 segment DS is distinguished from a File Allocation Unit (FAU) used in the context of file

1 systems for computer applications (a data segment DS can include an FAU). The latter
2 is usually contiguous but much smaller in size than a data segment DS. An FAU is
3 typically between e.g. 4096 and 8192 bytes (8 to 16 sectors) while a data segment DS
4 is between e.g. 131 Kbytes (256 sectors) to more than 1 Mbytes (several thousand
5 sectors or several tracks), or more. Preferably, the size of a data segment DS is
6 selected by balancing among factors including: (1) preservation of the (forward
7 sequential) throughput of the disk drive 25, (2) budgeting for error event management,
8 (3) the size (and cost) of a DRAM cache buffer 86 in the disk drive 25, and (4) retaining
9 the random access nature of the disk drive 25. An example selection can be two or
10 more tracks per data segment DS. Generally, though not necessarily, all data
11 segments DS are of the same size. The data segments can be of different sizes, but
12 with coherent phase.
13

14 In one version of the present invention, coherent phase can be forced on data
15 segments DS with the choice of number of tracks per data segment size. The cache
16 buffer size can also be adjusted to be larger than may otherwise be needed to take
17 advantage of random access storage. In disk drives utilizing Variable Frequency
18 Recording (VFR) (e.g., FIG. 6B), wherein track density varies radially, in recording
19 zones at the inner most tracks there are n sectors (blocks) per track and in recording
20 zones at the outer most tracks there are about $2n$ sectors per track. Tracks in between
21 vary substantially piece-wise linearly. Preferably, the number of tracks per data
22 segment DS is selected to vary linearly by radius. Therefore, a coherent phase data
23 segment layout according to the present invention for seek time, and worst case
24 response time or throughput performance, can be adjusted according to the possible
25 seek distances and times for each VFR location on the disk 38.
26

27 Advantageously, a coherent phase data segment layout of the present invention
28 provides the same forward and reverse sequential performance for data segment

1 access. The substantially identical performance for forward and reverse sequential
2 access is advantageous for AV 'trick-play' features, as for example fast-forward and
3 fast-reverse searching through an AV content object. As such, the present invention
4 affords more consistent response time, and thus data rates, particularly important for
5 fast-reverse searching.

6
7 FIG. 8A shows the disk drive performance for various e.g. intra-data segment
8 track skew angle α selections for data segments DS, and resulting inter-data segment
9 rotational phase R (inter-segment rotational time). FIG. 8B shows a plot of the ratio of
10 inter-segment rotational time on average seek time versus average data rate according
11 to the present invention. This example shows how to optimize the skew time for
12 performance, wherein:

13 skew time = $A \times T_{rev}$;

14 T_{rev} = time per revolution;

15 A = skew factor / skew modulus;

16 skew factor comprises the integer number of sectors that are
17 used to set the skew angle or time;

18 skew modulus comprises the integer number of sectors into
19 which a rotation of the disk is divided;

20 skew angle $\alpha = A \times 360$ degrees;

21 Rotation phase = $(1-A) \times 360$;

22 Rotation time = $(1-A) \times T_{rev}$ -- i.e. the time for the disk to rotate
23 to the start of the next Data segment DS;

24 Rotation on Avg Seek Time = (Rotation time) / (Avg seek time);

25 Avg Rate = (data per DS) /

26 (Avg Seek time + Rotation time + time to transfer data);

27
28 Skew modulus can be less than or equal to the number of servo samples per

1 track because a disk drive system cannot resolve time or phase below that interval. As
2 such, A is a rational number between 0 and 1. The skew modulus and the skew factor
3 are selected for a desired rotational time. Preferable selections provide an inter-
4 segment Rotational time to the next data segment DS of about the same as the
5 average seek time for the actuator servo system. This can be used as a guide for
6 designing data layout and seek profiles for disk drive systems with different parameters
7 and geometry.

8
9 Because the present invention offsets many negative aspects of rotational
10 latency, lower spindle speed disk drives are possible. As discussed herein, access time
11 is the sum of seek time and rotate time, response time is the sum of access time and
12 transfer time, and data rate is the ratio of the data transferred to the response time. As
13 rotate time after seek arrival (rotational latency) is nearly zero due to a data segment
14 layout according to the present invention, rotate time is effectively offset. Therefore,
15 slowing the spindle speed (increasing T_{rev}) has less impact on the data rate than for
16 conventional data layouts. Thus, lower speed disk drives can be used to support a
17 given quality or number of A/V streams. Lower speed disk drives further reduce total
18 acoustic noise.

19
20 The present invention improves the average performance of the disk drive 25 as
21 measured by data rate, response time and access time. Further, the present invention
22 significantly improves minimum data rates, maximum response time and maximum
23 access time. These improvements afford more budget for error event management
24 caused by environmental factors. External disturbances from shock or vibration can
25 lead to excessive servo position errors or track miss-registration. This can lead to
26 failure to read or write data on the first attempt. Read/write channel noise or spurious
27 debris inside the HDA can also lead to such failure. These failures, when they occur,
28 are referred to as error events and are usually managed by the disk drive in error

1 management events such that data are faithfully reproduced. Error management
2 consumes time for retry attempts and other remedies. Reducing the systematic time to
3 service A/V streams/data increases the time for managing error events and thus
4 improves total data reliability for a given A/V bandwidth requirement. Improving said
5 budget improves the reliability of disk drives in consumer applications by making them
6 more fit for such use.

7
8 In addition, the present invention systematically reduces the need for aggressive
9 time-optimal seek time performance using seek profiles described herein according to
10 the present invention. By making this reduction, the seek servo system is redesigned to
11 take advantage of the rotation time in a systematic manner. In so doing, additional
12 improvements can be gained in seek acoustic noise reduction from the disk drive. Still
13 further, demands on the disk drive system chassis design are reduced because seek
14 reaction forces are reduced (chassis response to seek reaction forces can result in
15 additional loss of response time performance.) This also makes the total acoustic noise
16 generated by a disk drive less dependent on the system chassis and enclosure. And
17 still further, the reduced performance required by the seek servo system allows the use
18 of lower cost power amplifiers and motors.

19
20 Additionally, using the coherent phase layout the present invention provides
21 symmetric forward and reverse sequential data segment DS access response time for
22 'trick-play' features. And, allows for reducing spindle spin speed with less loss in
23 average and minimum data rates than compared to conventional layouts such in FIG. 1.
24 Lower spin speed will further reduce total disk drive acoustic noise and imbalance
25 reaction forces transmitted through the system chassis that will still further reduce total
26 acoustic noise. Lower spin speed also allows for a lower total cost disk drive.

27
28 In addition to use of the disk drive 25 in computer applications, a data segment

1 layout according to the present invention allows the use of the disk drive 25 for storing
2 and retrieving AV content in e.g. an AV storage server. FIG. 9 shows a block diagram
3 of an example AV storage server 102 according to the present invention. The AV
4 storage server includes a controller 104, at least one buffer memory 106 and one or
5 more storage devices 108 such as disk drives 25. The disk drives 25 can store AV
6 information for various video titles such as movies. The controller 104 controls the
7 reading and writing of data to the disk drives 25. AV data read from one or more of the
8 disk drives 25 can be output from the AV storage server for display on a display 110
9 connected to the AV storage server 102. Each disk drive 25 can simultaneously or
10 sequentially provide AV information to one or more AV streams output from the AV
11 storage server 102. Further, incoming AV data into the AV storage server 102 (e.g.
12 from a cable or over a network) can be stored onto one or more of the disk drives 25.
13 Each disk drive 25 can simultaneously or sequentially store AV information from one or
14 more AV streams incoming into the AV storage server 102.

15
16 The AV storage server 102 can be a component of an AV system 112 according
17 to the present invention, wherein the AV system 112 comprises said AV storage 102
18 including an interface unit 114, a wired communication network 116 and component
19 boxes 118 such as consumer electronics equipment. The AV system 112 can be a part
20 of a home network system with connection to external cable or network for receiving AV
21 information. One or more component boxes 118 can receive AV content from one or
22 more disk drives 25 in the AV server. AV data is read from the disks 38 in the disk
23 drives 25 to produce data streams which are transmitted to component boxes 118 or
24 displays 110 for viewing. Further, one or more component boxes 118 can provide AV
25 content to one or more disk drives 25 in the AV server 102. The data streams can
26 comprise sequences of discrete fragments of data which are periodically transmitted in
27 bursts so that over time a constant stream of data is transmitted. The data stream can
28 be smoothed out using the buffer memory 106. In one example, each burst of video

1 data can corresponds to about 0.5 to 1.0 second of video for a magnetic disk drive 25.

2
3 The present invention improves the systematic reliability of response time and
4 consequently the sustained data rate or throughput, taking into account the electro-
5 mechanical nature of the disk drives 25. At the same time the present invention
6 preserves the random access nature of a disk drive, and the new benefits derived
7 therefrom for the storage of AV content described above.

8
9 In another aspect the present invention provides a method for storing a stream of
10 data to the data storage disk so as to obtain a nearly constant data storage transfer rate
11 when reading the data from the data storage disk. FIGS. 10-11 show example
12 flowcharts of processes for implementing an embodiment of the of present invention.
13 To best understand the processes, the reader should also refer to FIGS. 2-4. In one
14 implementation, the processes can take the form of computer programs typically
15 executed by well-known data processing and control electronics including
16 microprocessors or microcontrollers 80, 14 (e.g., PD787012 microcontroller
17 manufactured by NEC). In one example, the computer programs can be executed by
18 the microcontrollers 80 or 92 in response to data storage and retrieval commands from
19 a processor 14. From the depicted flow charts, those skilled in the art can readily select
20 an appropriate microcontroller and program the selected microcontroller to execute the
21 disclosed processes.

22
23 Referring to FIG. 10, in one embodiment, the method of storing a data stream
24 includes the steps of partitioning incoming data into data segments (step 120); moving
25 the data segments into a buffer 86 or 16 (step 122); for each data segment: identifying
26 one or more data tracks on the data storage disk to store a data segment (step 124);
27 directing the VCM to position the transducer over the identified tracks (step 126); and
28 recording that data segment in the identified tracks with coherent phase relative to other

1 data segments according to the data segment pattern herein, wherein the start phase
2 for each data segment is the same, and the end phase for each data segment is the
3 same (step 128). In one example, steps 120, 122 can be executed by the processor
4 14, and steps 124, 126 and 128 can be executed by the drive electronics 26 (e.g.,
5 microprocessor 80 or data controller 92). Other functional division of the computer
6 program for execution by various processors and microcontrollers in the disk drive 25
7 and other system processors such as the CPU 14 are also possible and contemplated
8 by the present invention.

9
10 As such, in one scenario, a data stream is received by the disk drive 25 as bytes
11 of data, and segmented into sectors. A set of sectors is grouped into a data segment
12 by the disk drive electronics chip 84. The data segment is stored into a disk drive buffer
13 86, and the microprocessor 80 identifies the tracks on the data storage disk 38 to store
14 the data segment. The servo controller 94 positions the transducer 64 over the
15 identified tracks, and the data segment is stored onto the tracks according to the
16 coherent phase data segment pattern/layout herein.

17
18 In step 128, the data segments can be recorded such that the data tracks in
19 each data segment are offset by a predetermined skew angle α . Preferably, the skew
20 angle is selected to minimize rotational latency as the transducer is positioned over
21 adjacent tracks within a data segment to write data thereto and later read data
22 therefrom, in a forward sequential fashion. Due to the coherent phase layout/pattern,
23 the data segments are recorded so as to obtain a nearly constant data storage transfer
24 rate when transferring data to/from the storage media (e.g., data disk). In another
25 embodiment of the present invention, the steps shown in FIG. 10 can be performed,
26 sequentially or simultaneously, to store multiple data streams onto the data disk drive
27 25.

1 Referring to FIG. 11, the data segments can be read from the data disk 25 in
2 response to data segment requests from a processor 14. Upon receiving a request for
3 a data segment stored on the disk 38 (step 130), the data tracks where the data
4 segment is stored are identified (step 132); the VCM is directed to position the
5 transducer over the identified tracks (step 134); and data is read from the identified
6 tracks into a buffer 86 or 16 to be transmitted to the requester (step 136). The data
7 segments read from the disk can be combined to reformulate an output one or more
8 data streams (step 138).

9
10 As such, in one scenario, the data segments can be read from the disk drive 25
11 in response to host command(s). The disk drive electronics chip 84 receives the
12 request for data segment. The microprocessor 80 identifies the location of the data
13 tracks on the disk media where the data segment resides, and the servo controller 94
14 positions the transducer 64 over the identified tracks. The data is read, in form of a data
15 segment, one sector at a time, from the tracks and stored into the disk drive buffer 86.
16 When a certain amount of data has been stored in the buffer 86, the data is
17 reformulated and transmitted across the disk drive interface.

18
19 The steps in FIG. 11 can be performed, sequentially or simultaneously, to read
20 data segments for multiple data streams from the disk, and reformulate the data
21 streams for output. In one example, steps 130 and 138 can be executed by the
22 processor 14, and steps 132, 134 and 136 can be executed by the drive electronics 26
23 (e.g. microprocessor 80 or data controller 92). Other functional division of the computer
24 program for execution by various processors and microcontrollers in the disk drive 25
25 and other system processors such as the CPU 14 are also possible and contemplated
26 by the present invention. The processes described in relation to FIGS. 10-11 can also
27 be implemented in the system 102 of FIG. 9.

1 The data/track layout method of the present invention allows organizing and
2 allocation of disk drive capacity when used to store and retrieve isochronous Audio-
3 Video (AV) content for consumer electronics applications. The layout model can be
4 particularly useful when managing multiple AV streams over multiple content objects in
5 a full and fragmented disk drive. The method is not dependent on any particular
6 interface, and can be implemented on e.g., ATA, SCSI, 1394, etc.. Using FCP-AV/C
7 with an on board Stream Manager and Embedded File System can be used with the
8 method of the present invention e.g. for content objects used for delay broadcast
9 applications.

10
11 A coherent phase layout according to the present invention is laid out on the disk
12 38 such that data can be stored on the disk 38 according to that phase layout (e.g., the
13 segments have coherent relative rotational phase). Servo information provides track
14 location and angular position of data on the disk 38, allowing specification of an
15 address for every sector on the disk, wherein a collection of sectors makes up a
16 segment DS. Therefore, the servo information provides radial position and angular
17 position of each segment DS on the disk 38. The start of each track is recorded on the
18 disk 38 at fabrication, such that when data is later written in segments DS on the disk
19 38 by the servo system, the data is stored in segments having relative coherent phase.
20 As such, the coherent phase pattern is an attribute of the fabricated layout pattern on
21 the disk 38. In one example, the disk 38 of the disk drive 25 is initially formatted with S-
22 Scan using the coherent phase pattern layout described herein and shown by example
23 in the drawings. Thereafter, an LBA to physical transformation module in the disk drive
24 firmware maps data segments according to the coherent phase data segment pattern
25 herein.

26
27 According to the present invention, the rotational phase between the end of one
28 data segment to the start of another data segment, has a limited range of

1 predetermined possibilities or in a special case it is constant. The servo system in
2 implemented with information representing rotational time from end of one data
3 segment to start of another data segment. According to the present invention, a data
4 segment pattern with coherent phase is laid out on top of data layout, which is on top of
5 the servo format. In conventional data layouts, there is no linkage from the servo
6 system to the data layout, making it impossible to determine rotational time between
7 end of a piece of data to start of another piece of data.

8
9 In one example operation, a read/write command is issued and translated into a
10 specific sector or sectors which start at a logical block address. The logical block
11 address is converted (mapped) to a physical location on the disk surface using
12 information including e.g. the size of each data segments, number of tracks per data
13 segment, number of sectors per track, mapping of the defects, track skew angle, etc..
14 Firmware, or software that controls hardware, uses said information to convert logical
15 block addresses to physical locations based on the coherent phase layout according to
16 the present invention. The disk drive 25 then uses head(s) within the head transducer
17 assembly 64 for writing and reading magnetic patterns on the rotating magnetic storage
18 disk 38 in the physical locations in one or more concentric tracks. A coherent phase
19 segment layout/pattern according to the present invention includes segments DS such
20 that each segment DS has a predetermined rotational phase relative to another
21 segment DS.

22
23 For example, said relative rotational phase R for each data segment in FIG. 5 is
24 the same (i.e., $360-\alpha$). The predetermined relative rotational phase R from a data
25 segment to different data segments can be different and selected from a limited number
26 of predetermined values. In the example LBAX and data segment DS layout described
27 herein in relation to FIGS. 5-6, each data segment DS includes two tracks, and has the
28 same relative rotational phase R (i.e., $360-\alpha$) to other data segments. However, a data

1 segment DS need not be larger than one track. This is particularly the case as linear
2 densities for disk drives increase. Further, some applications are limited to less than
3 one track.
4

5 For example, when using ATA disk drives, there is a standard limit of 256 sectors
6 per command, which can be utilized as the size of a data segment DS. Referring to
7 FIG. 12A, as an example, each data segment DS can be selected to be $\frac{1}{2}$ track in
8 length. In that case, there are two different predetermined relative rotational phases for
9 each data segment DS. Predetermined rotational times (rotational phases R) from
10 each data segment to another segment can be one of: (1) $\frac{1}{2}$ a full rotation time ($R=180$
11 degrees), or (2) a full rotation time (i.e., $R=\text{one revolution or } 360 \text{ degrees}$). The track-
12 to-track seek time or skew angle is not applicable in this case, and the one track and
13 head switch time are not necessary.
14

15 As such in the example data segment pattern shown in FIG. 12A, each data
16 segment has a first relative rotational phase R relative to a first set of data segments,
17 and a second relative rotational phase R relative to a second set of data segments. For
18 example, the data segment DS0 has a first relative rotational phase $R=360$ degrees to
19 a first set of data segments including data segments DS2, DS4, DS6, etc. And, the
20 data segment DS0 has a second relative rotational phase $R=180$ degrees to a second
21 set of data segments including DS1, DS3, DS5, DS7, etc.. Similarly, the data segment
22 DS0 has a relative start phase of 180 degrees relative to data segments DS1, DS3,
23 DS5, etc.. And, the data segment DS0 has a relative end phase of 180 degrees with
24 relative to data segments DS1, DS3, DS5, etc.. Similarly, the data segment DS0 has a
25 relative start phase of 0 degrees relative to data segments DS2, DS4, DS6, etc.. And,
26 the data segment DS0 has a relative end phase of 0 degrees with relative to data
27 segments DS2, DS4, DS6, etc..
28

1 Further, all tracks need not have the same number of segments DS because
2 there can typically be several (e.g., 16) recording zones (e.g., FIG. 6B) that include
3 different number of sectors per track. As such, the number of segments DS in each
4 track can vary from zone to zone. For example, the number of different relative start,
5 end and rotational phases for data segments in each zone can increase from zones at
6 the inner diameter to the zones at the outer diameter of the disk surface. The number
7 and values of relative start, end and rotational phases for data segments in each zone
8 can be selected as described by example herein to obtain one or more of the
9 advantages of the layout format described herein.

10
11 As such, each zone includes a set of data segments therein with coherent phase
12 selected for that set. In FIG. 6B, two of a plurality of recording zones 91 are shown.
13 The inner recording zone includes an example data pattern layout such as shown in
14 FIG. 12A wherein each track 74 includes two data segments. The outer recording zone
15 includes an example pattern layout such as in FIG. 5 including data segments DSi and
16 DSj where each data segment includes two tracks 74, and further shows start S and
17 end E of each of the data segments DSi and DSj. Though in FIG. 6B, the layout
18 patterns in the inner and the outer recording zones are different, in other versions the
19 layout patterns in the recording zones can be the same wherein: in one case in all
20 recording zones 91 each data segment DS includes one or more tracks 74, in another
21 case in all recording zones 91 each track 74 includes two or more data segments DS,
22 etc..

23
24 FIG. 12B shows another embodiment of a data pattern layout in a linear fashion,
25 according to the present invention, wherein each data segment is one-third track in
26 size. For example, track TKn+2 comprises three data segments DS56, DS57 and
27 DS58 (track and DS numbers are randomly selected for purposes of example only). In
28 one version, in a disk with multiple recording zones (e.g., FIG. 6B), a first zone on a

1 recording surface of the disk can include the pattern layout of FIG. 12A and a second
2 zone on that recording surface can include the pattern layout of FIG. 12B. Yet in
3 another version, where the disk includes multiple recording zones, and each data
4 segment DS includes one or more tracks (e.g., FIG. 5), the number of tracks per data
5 segment can be different in different recording zones. Further, the present invention
6 contemplates a recording surface with different recording zones, wherein in at least one
7 recording zone each data segment includes one or more tracks (e.g., FIGS. 12A-B),
8 and in at least another recording zone each track includes two or more data segments
9 (e.g., FIG. 5).

10
11 For pattern layouts such as shown by example in FIGS. 12A-B, where the
12 relative rotational phase between segments has one of several predetermined values,
13 the seek time for seek operations between segments in each recording zone can have
14 one or more predetermined time values based at least on the values of the relative
15 rotational phases (e.g., seek time can be a linear function of inter-segment rotation
16 time). The seek time can also be selected to be a maximum, minimum, or combination
17 of said predetermined time values for all seek distances. For example, for the pattern
18 of FIG. 12A, the seek time can comprise the predetermined rotational times from each
19 starting data segment to a destination data segment, including: (1) time for $\frac{1}{2}$ a full disk
20 rotation time ($R=180$ degrees), or (2) time a full disk rotation time (i.e., $R=\text{one revolution}$
21 or 360 degrees).

22
23 The seek time for each seek operation between two segments in FIG. 12A is
24 obtained based on the rotational time from the track where the starting data segment
25 ends to the track where the destination data segment starts. In one example, the
26 predetermined seek time between each pair of segments (e.g., identified by unique
27 number or location on reference disk) is stored in memory to access and use by the
28 servo loop. For example the servo controller 94 can access a seek time table (stored in

1 memory 86, or within the servo controller 94, or on the disk, etc.) for each recording
2 zone, wherein each seek time table includes information representing the
3 predetermined seek time for each pair of data segments in that recording zone. In
4 another example, the predetermined seek time between each pair of segments is
5 obtained from the inter-segment rotational time between the two segments based on
6 e.g. the angular location of the end of the starting segment and the angular location of
7 the start of the destination segment on the recording surface of the disk.

8
9 Though in the embodiments described herein a disk drive is used as an example
10 of a data storage system or data storage device, other examples are possible. For
11 example, the present invention can be implemented in other data storage devices such
12 as e.g. CD player, DVD, CD ROM, etc.

13
14 The present invention has been described in considerable detail with reference
15 to certain preferred versions thereof; however, other versions are possible. Therefore,
16 the spirit and scope of the appended claims should not be limited to the description of
17 the preferred versions contained herein.